

; GTX%&1&-50MHz-5  
TCXO  
Low Noise CMOS

## Features

50 MHz Operating Frequency  
Low phase noise :  
Better than -145 dBc/Hz at 1 KHz  
Better than -155 dBc/Hz at 10 KHz  
11.4 x 9.6 x 1.85 mm SMD Package  
3.3V; HCMOS output

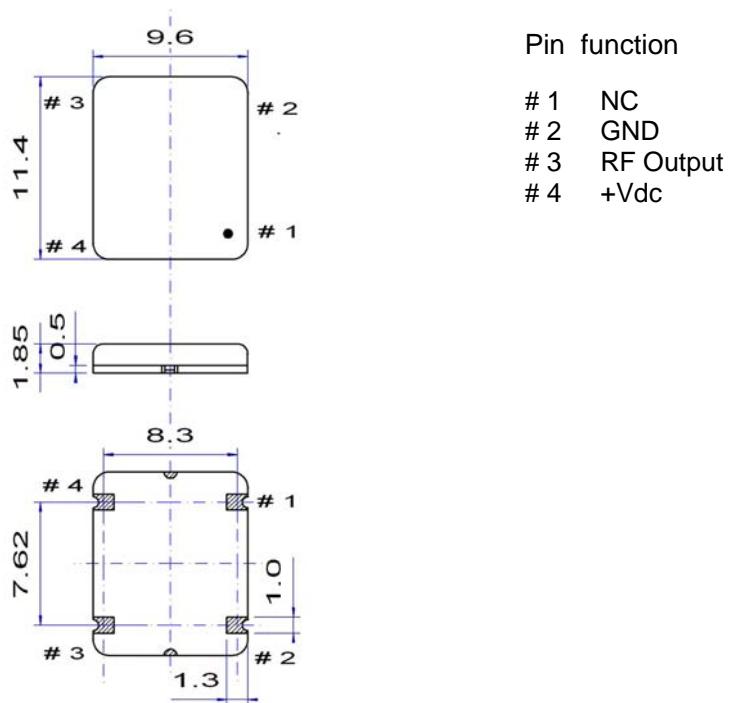
## Typical Applications

Test Instrumentation  
Microwave Communications

## Description

The GTX%&1&-50MHz operating at 50MHz offers a very low noise HCMOS output based on continuous analog temperature compensation.

## Mechanical Drawing and PIN Connections

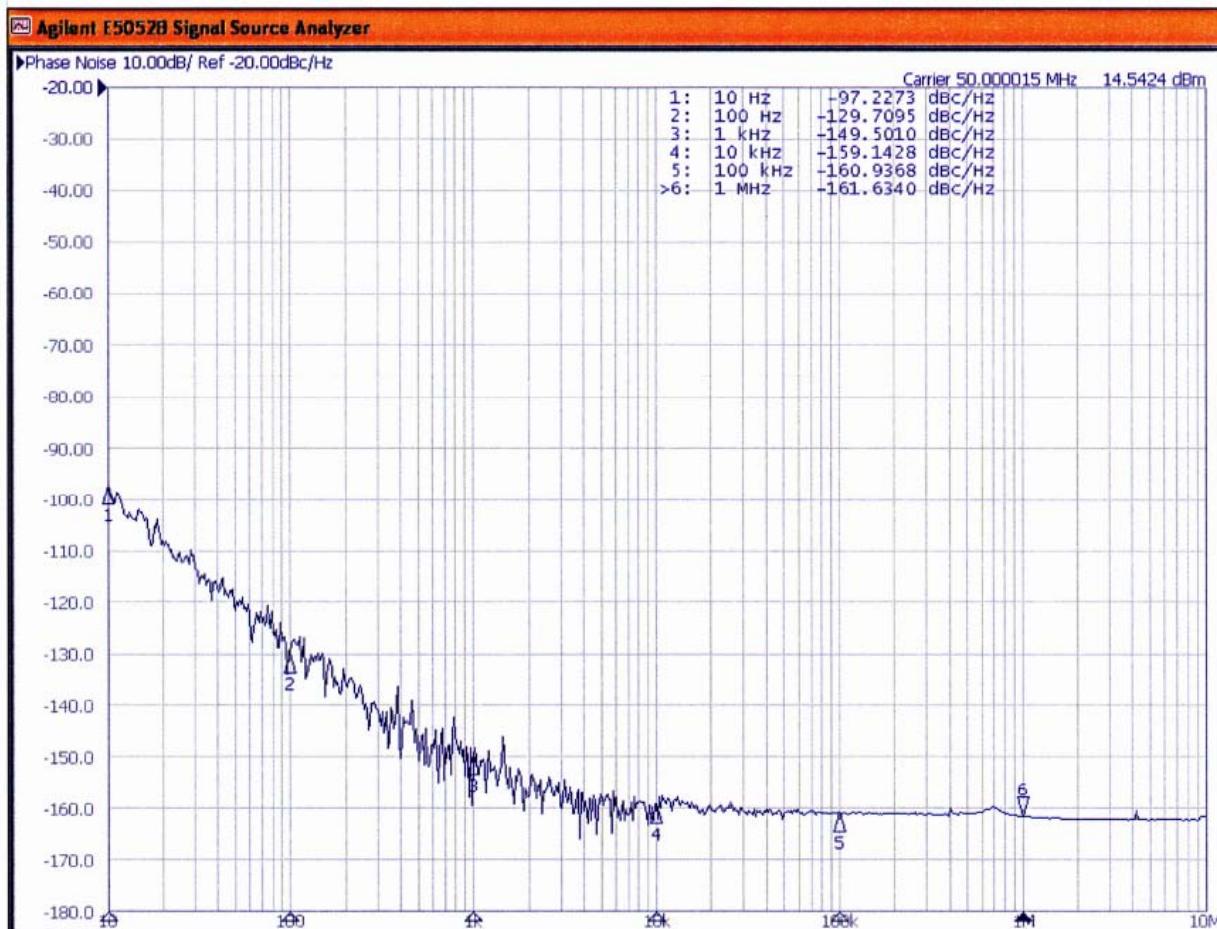


## Specification

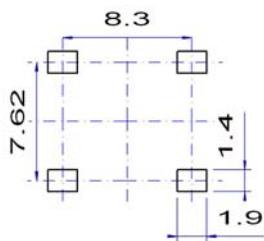
| TCXO Specification                               | Sym.              | Condition               | Value      |            |       | Unit   | Note |
|--------------------------------------------------|-------------------|-------------------------|------------|------------|-------|--------|------|
|                                                  |                   |                         | Min.       | Typ.       | Max.  |        |      |
| <b>Operational Frequency Range</b>               | $f_0$             |                         |            | 50         |       | MHz    |      |
| HCMOS/<br>TTL<br>compatible<br>option<br><br>N/A | Load              |                         |            | 15         |       | pF     |      |
|                                                  | H - level voltage | $V_H$                   | 0.9* $V_C$ |            |       | V      |      |
|                                                  | L - level voltage | $V_L$                   |            | 0.1* $V_C$ |       | V      |      |
|                                                  | Rise & Fall time  |                         |            | 10         |       | ns     |      |
|                                                  | Duty cycle        |                         | 40         | 50         | 60    | %      |      |
| <b>Power supply</b>                              |                   |                         |            |            |       |        |      |
| Voltage                                          | $V_{cc}$          |                         | 3.150      | 3.300      | 3.450 | V      |      |
| Current consumption                              |                   |                         |            | 10         | mA    |        |      |
| <b>Frequency stability</b>                       |                   |                         |            |            |       |        |      |
| vs. temperature                                  |                   | From -20C to 70C        | - 2.5      |            | + 2.5 | PPM    |      |
| Tolerance at 25C ;                               |                   | 24 hrs after REFLOW     | - 1.5      |            | + 1.5 | PPM    |      |
| vs. 5% change in supply voltage                  |                   |                         | - 0.5      |            | + 0.5 | PPM    |      |
| First Year Aging                                 |                   | After 30 days operation | - 1.0      |            | + 1.0 | PPM    |      |
| <b>SSB Phase noise<br/>At 50 MHz HCMOS</b>       |                   | 10 Hz                   |            | -97        |       | dBc/Hz |      |
|                                                  |                   | 100 Hz                  |            | -129       |       |        |      |
|                                                  |                   | 1KHz                    |            | -149       |       |        |      |
|                                                  |                   | 10KHz                   |            | -159       |       |        |      |

## Performance Graph

### Phase noise @ 50 MHz carrier frequency



Example for solder pattern



Example for IR reflow soldering temperature

